	Туре	L#	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	1 <b>3</b>	700/100.ccls. and (dynamic same plan\$6) and gui	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:12
8	BRS	L8	1 /	700/100.ccls. and (dynamic same plan\$6) and gui and (processors)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:13
9	BRS	L9	1 /	700/100.ccls. and (dynamic same plan\$6) and gui and (processors) and data	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:14
10	BRS	L10	1 /	700/100.ccls. and (dynamic same plan\$6) and gui and (process\$6 same data)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:14

	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	23	700/100.ccls. and (processor same plan\$5)	USPAT	2006/09/20 12:06
2	BRS	L2	30	700/100.ccls. and (processor same plan\$5)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:07
3	BRS	L3	23	700/100.ccls. and (processor same plan\$5) and (data same processor)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:09
4	BRS	L4	2	700/100.ccls. and (processor same plan\$5) and (data same processor) and simulation	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:08
5	BRS	L5	0	700/100.ccls. and (processor same plan\$5) and (data same processor) and simulation and gui	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:08
6	BRS	L6	2	700/100.ccls. and (processor same plan\$5) and (data same processor) and gui	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	2006/09/20 12:12